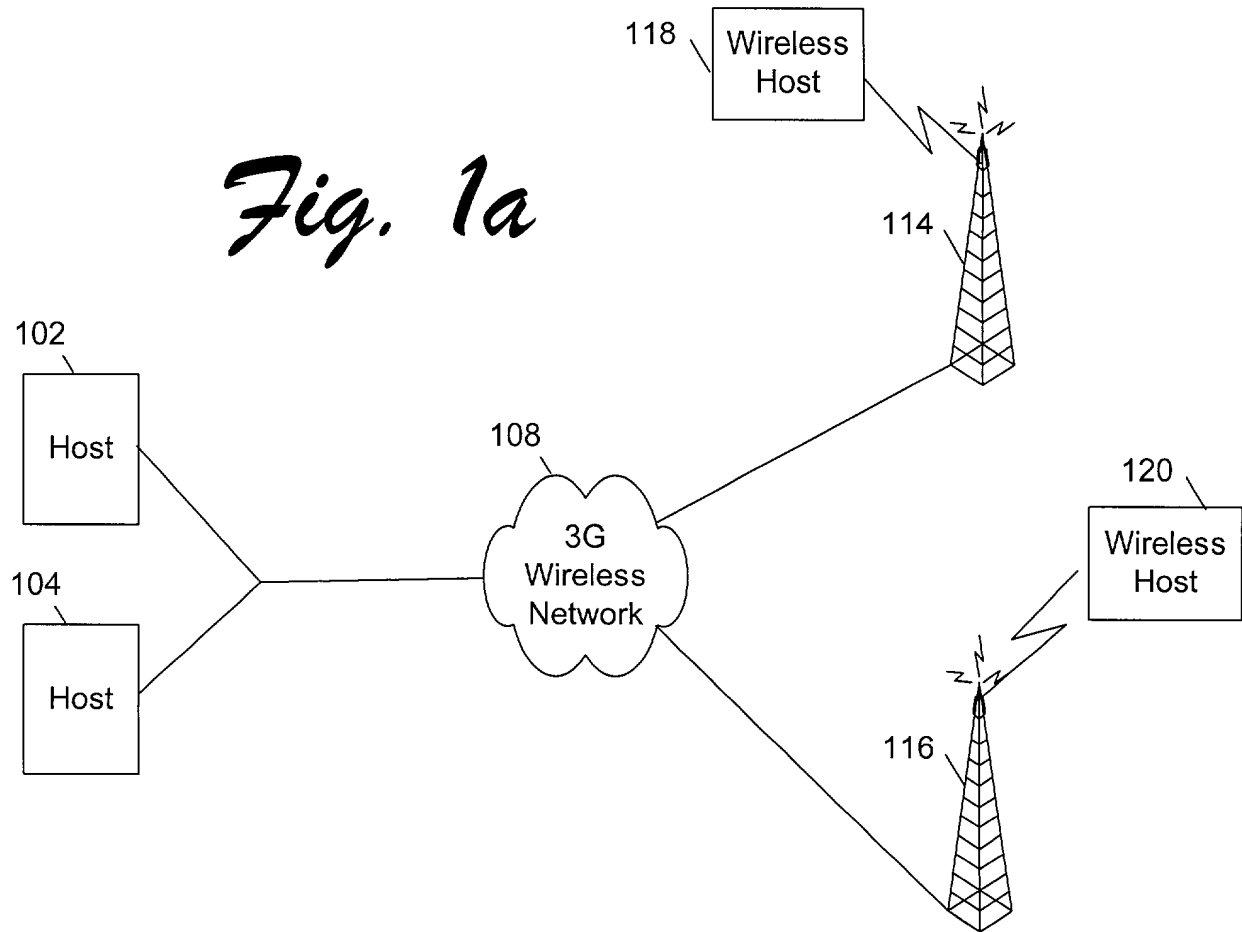
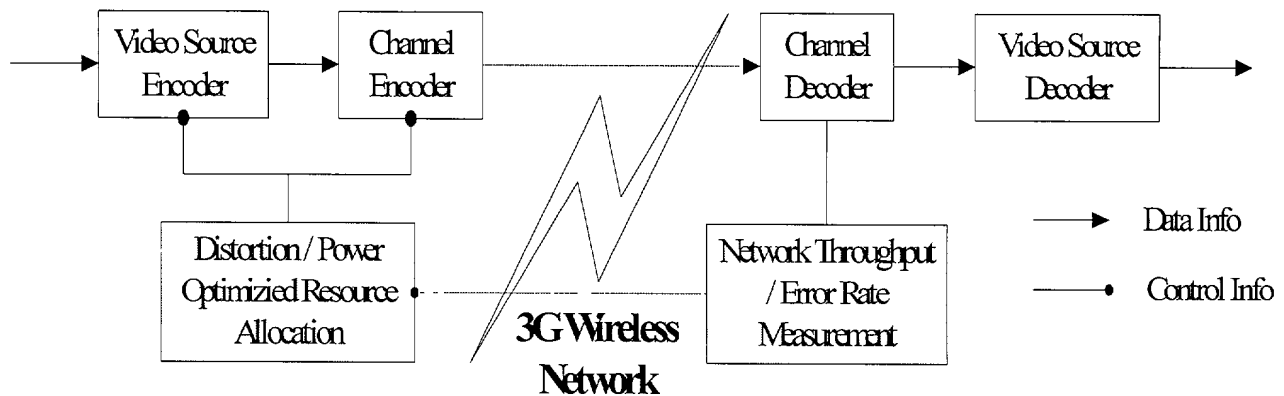


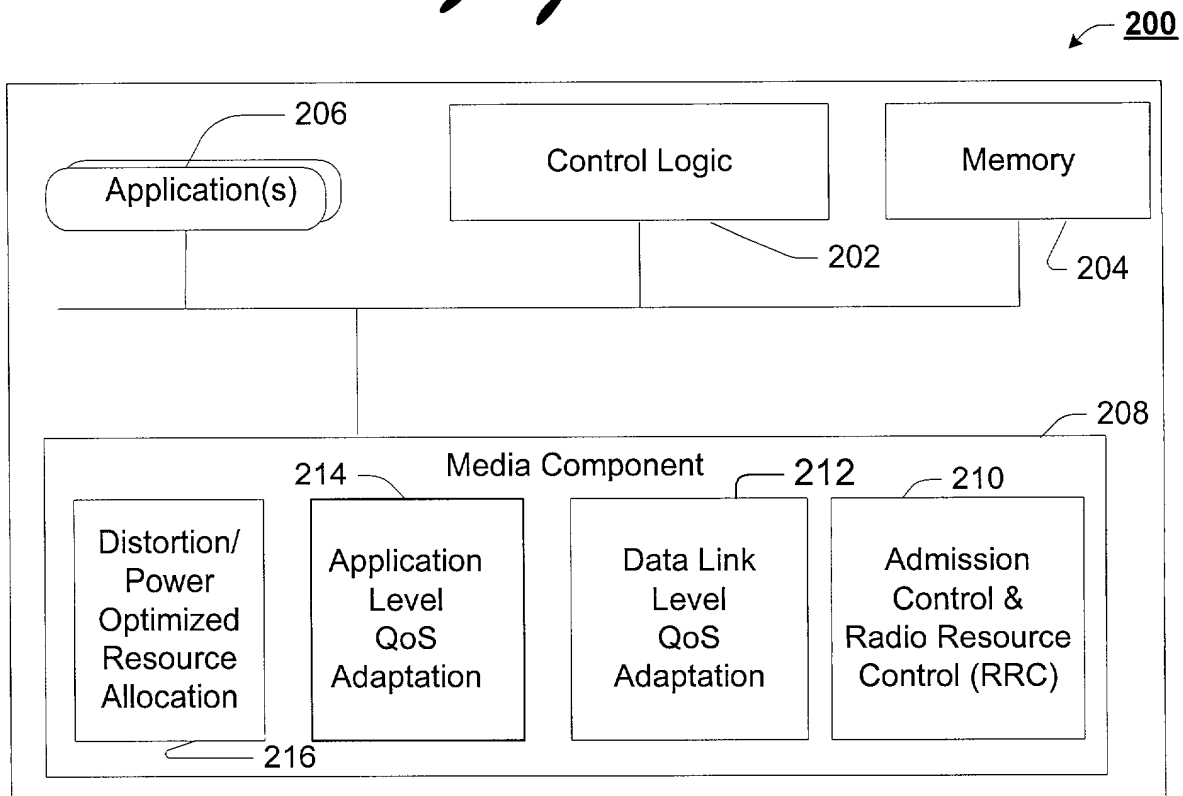
*Fig. 1a*



*Fig. 1b*



*Fig. 1c*



*Fig. 1d*

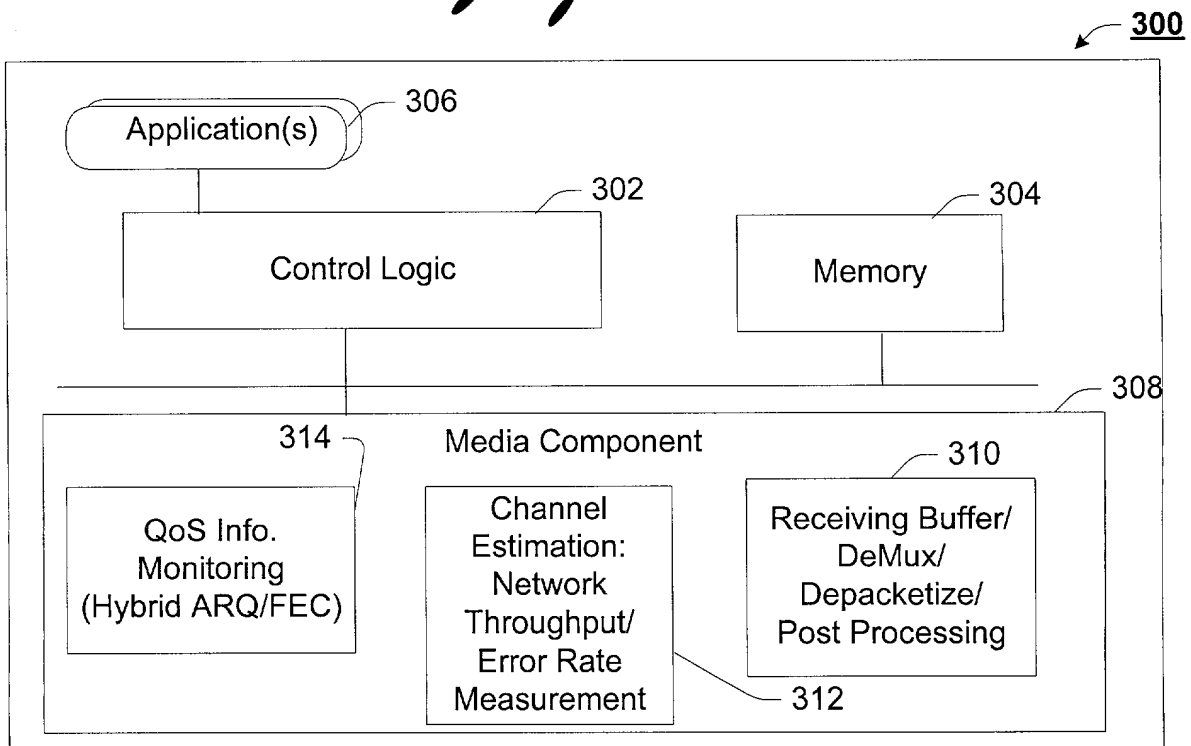


Fig. 2

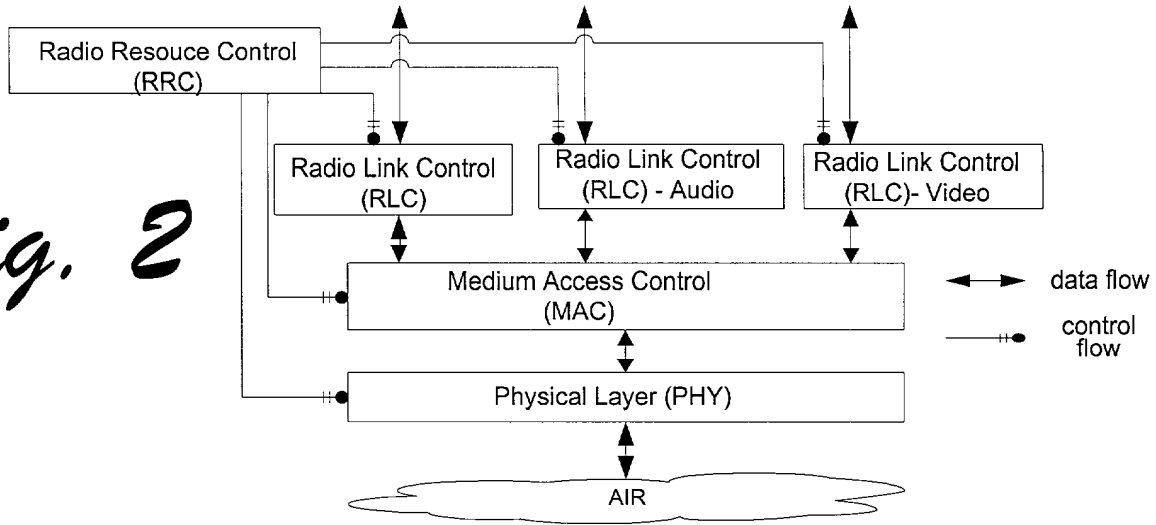


Fig. 3

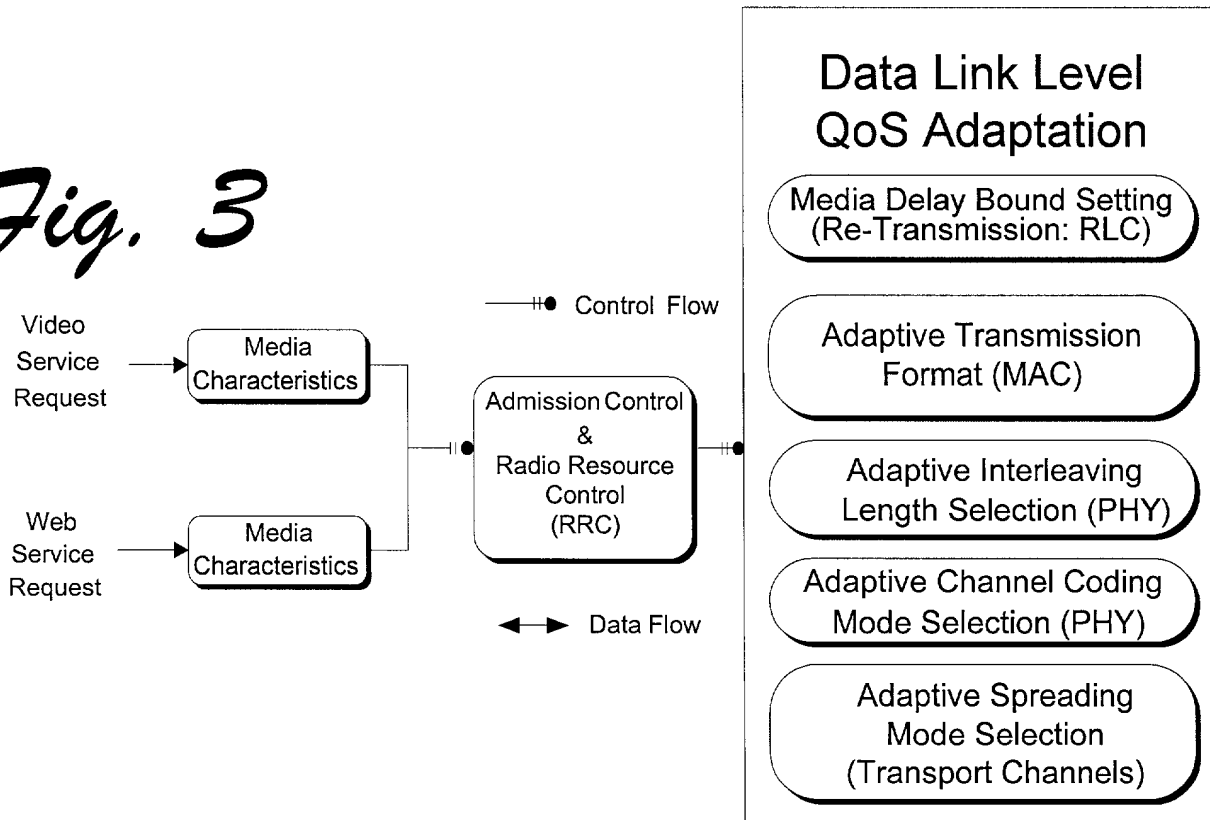
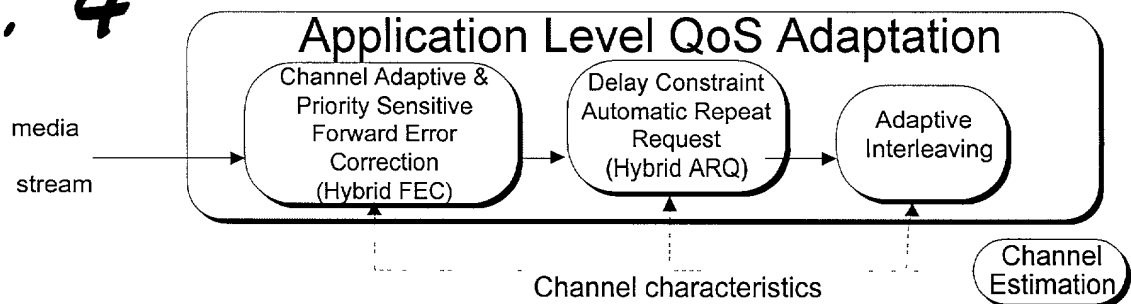
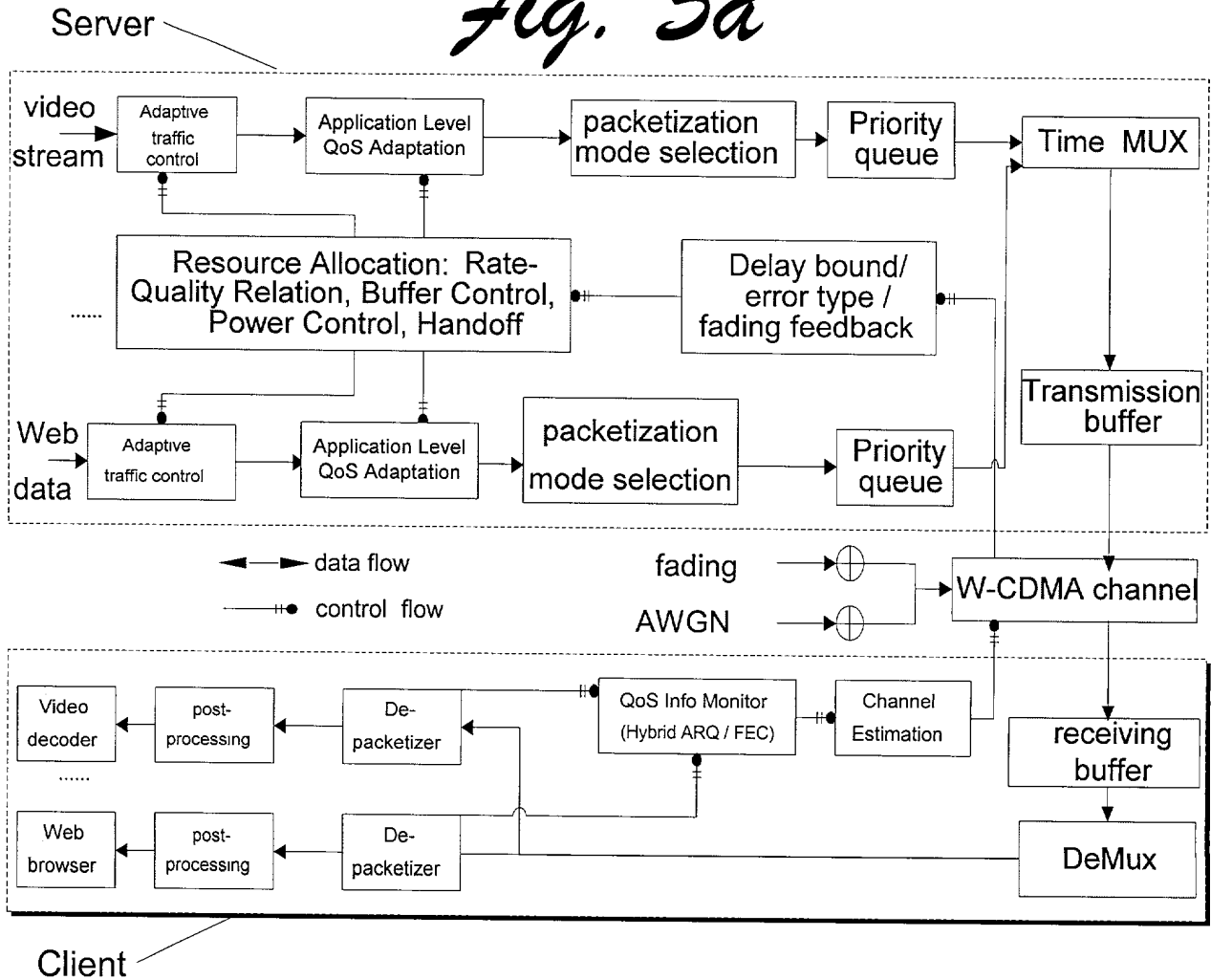


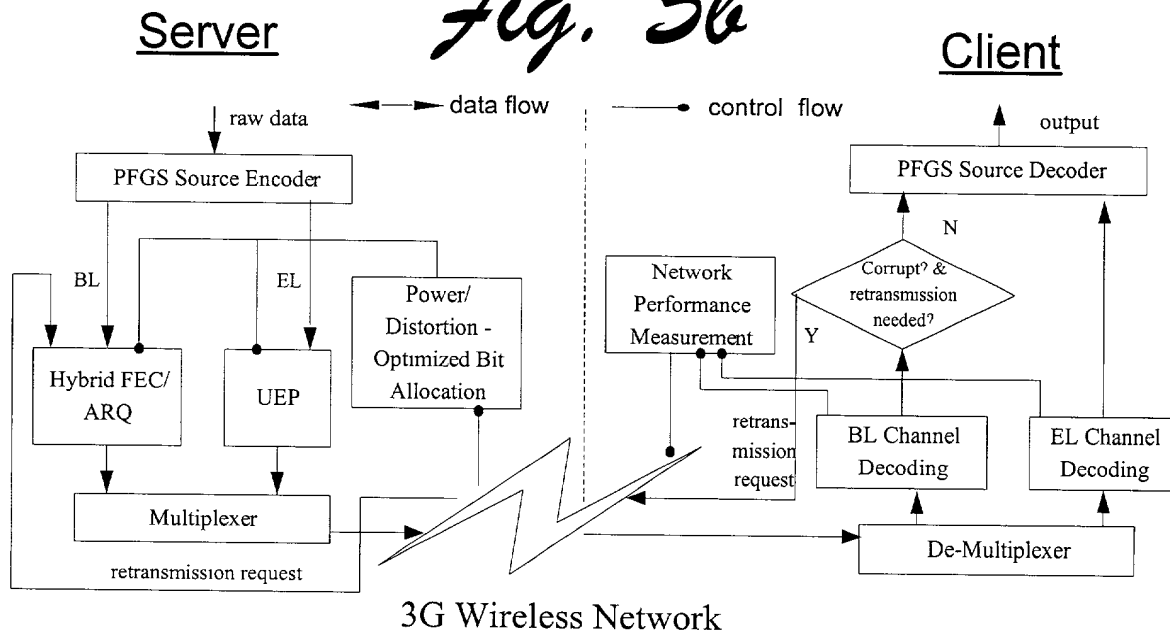
Fig. 4



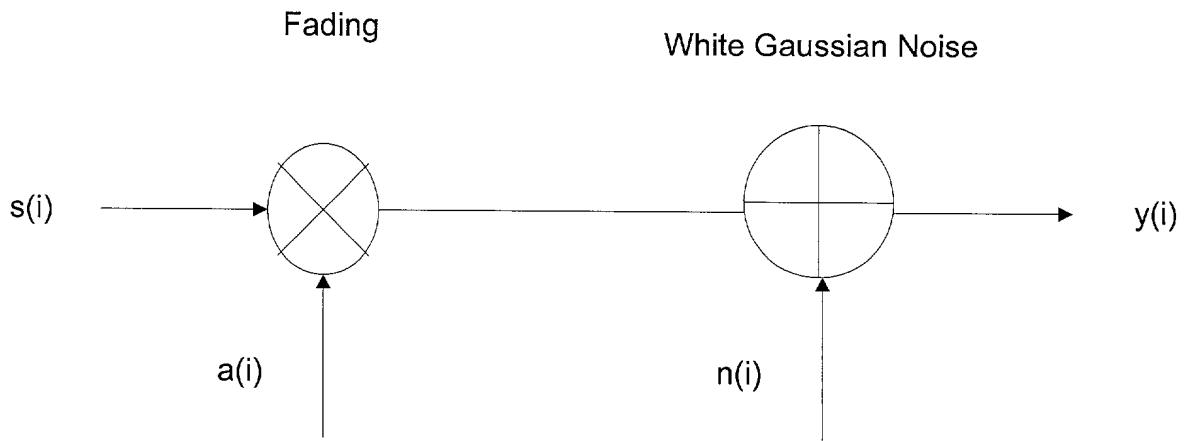
*Fig. 5a*



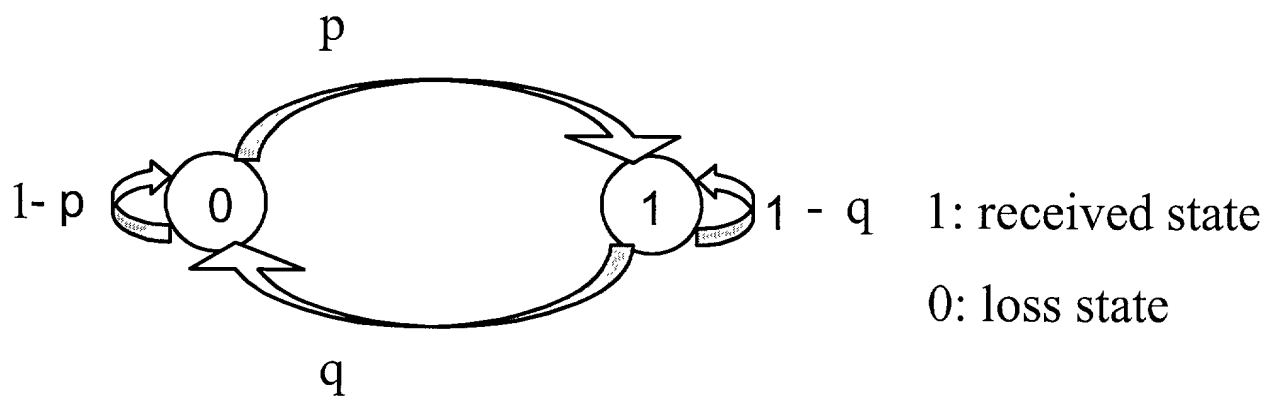
*Fig. 5b*



*Fig. 6*



*Fig. 7*



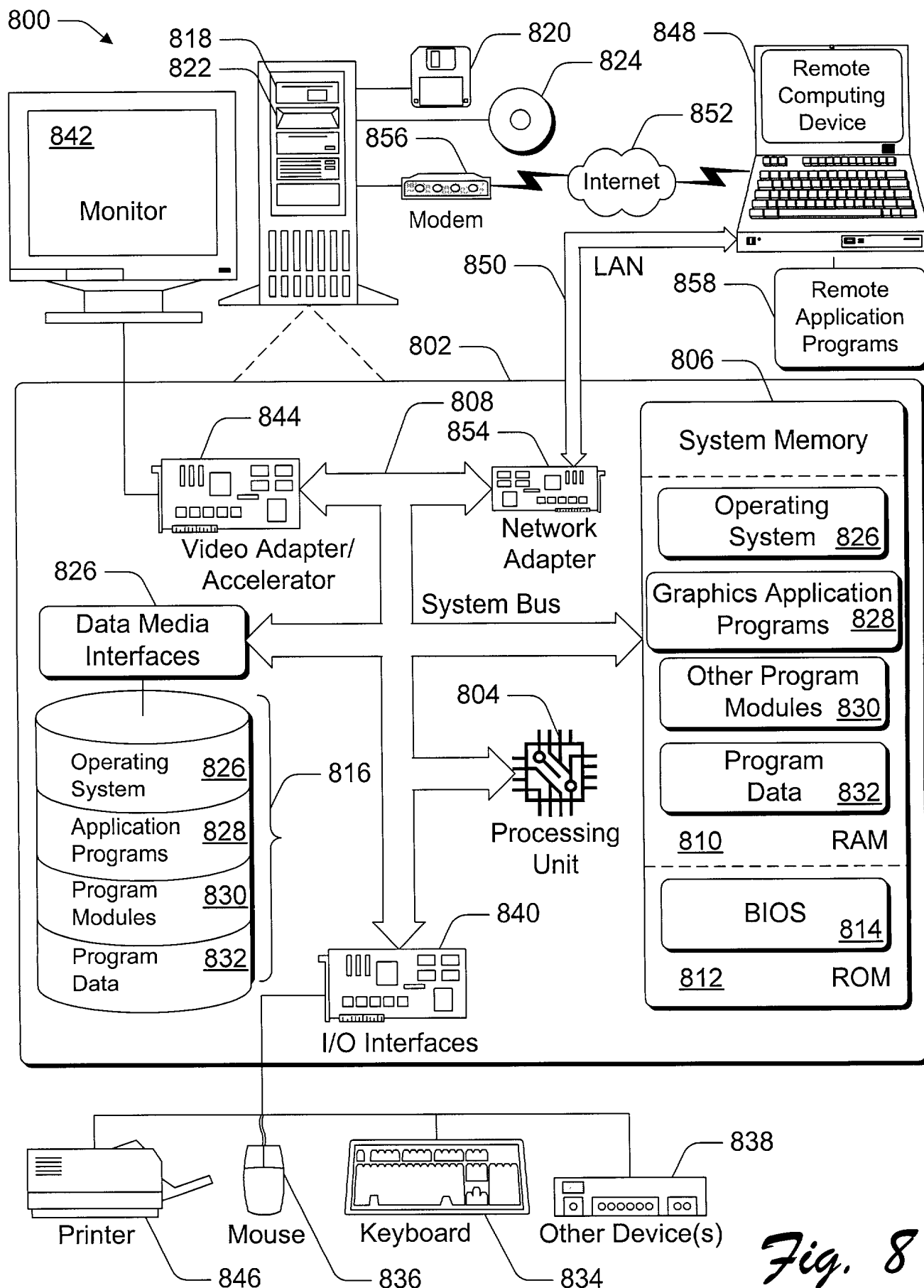
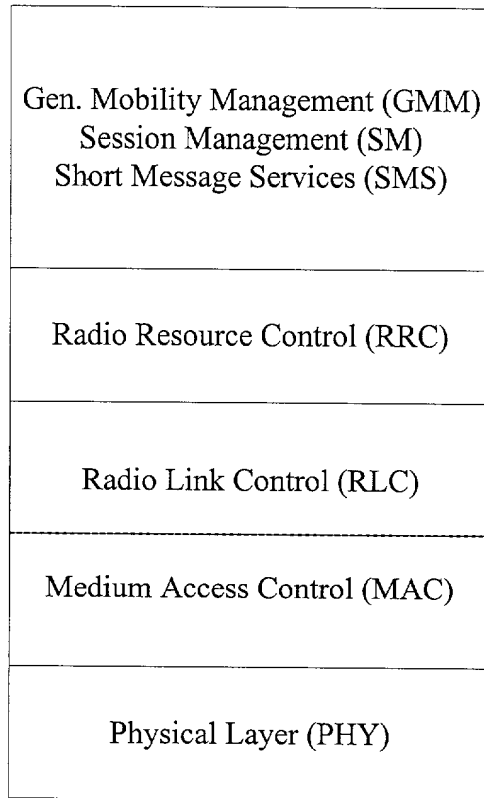


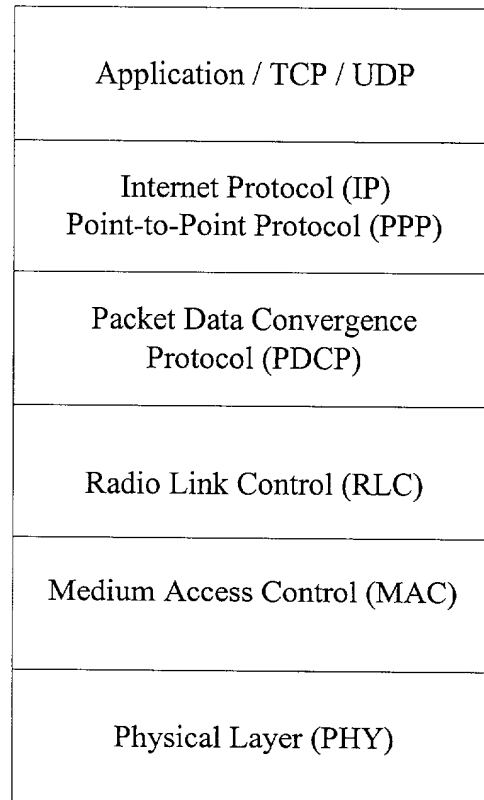
Fig. 8

*Fig. 9a*



Control Plane Protocol Stack

*Fig. 9b*



User Plane Protocol Stack

*Fig. 10*

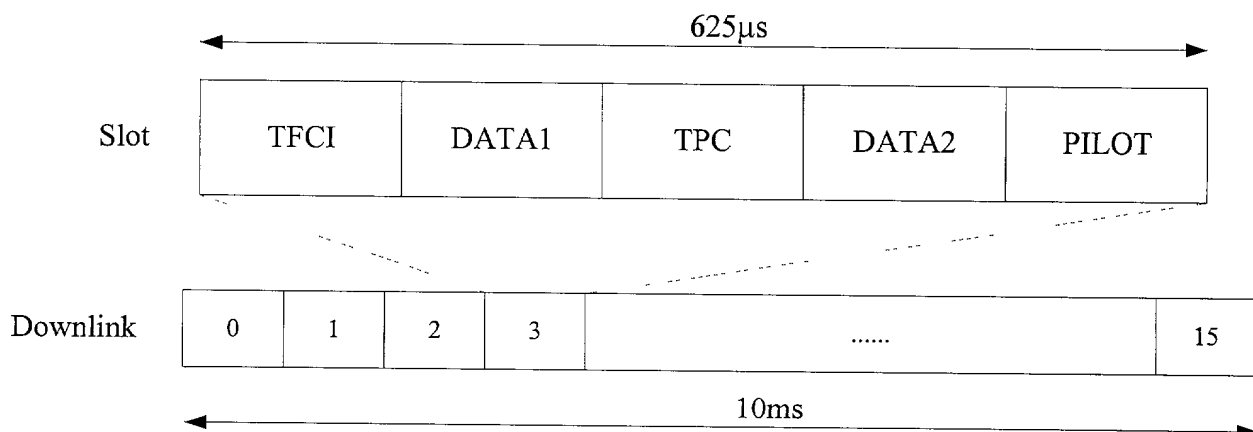


Fig. 11

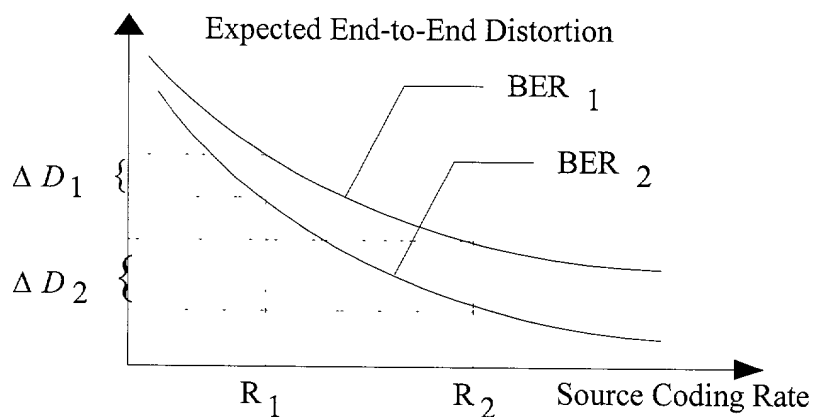


Fig. 12

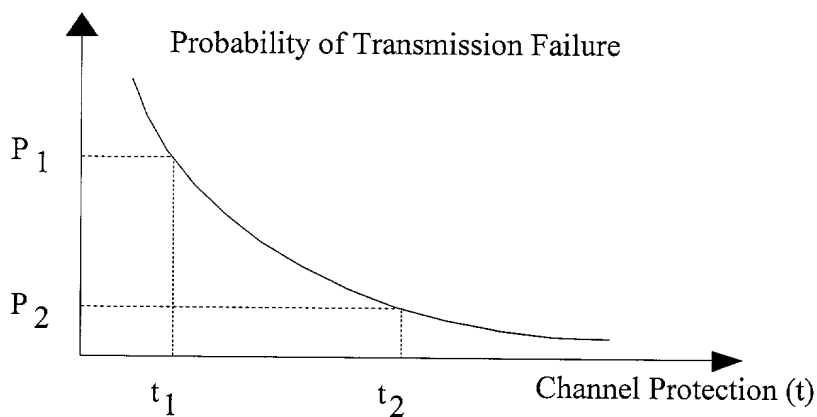


Fig. 13a

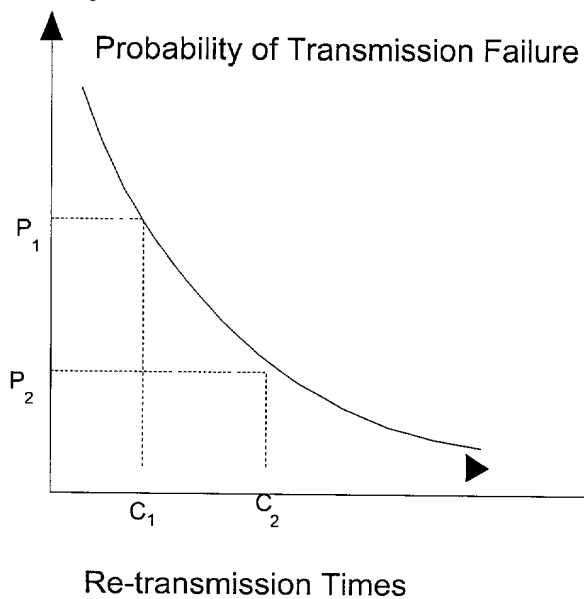


Fig. 13b

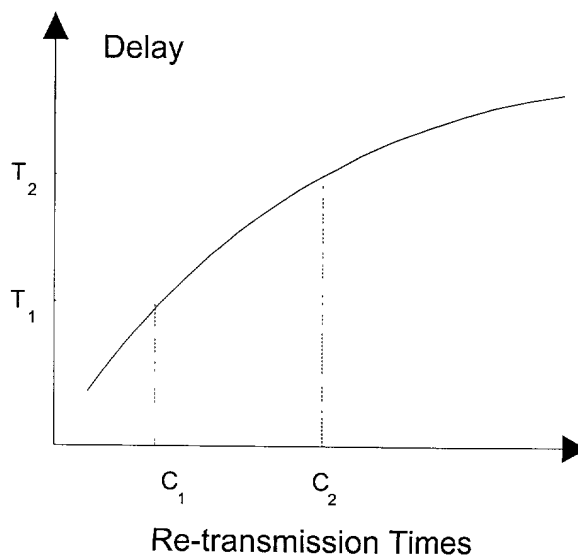




Fig. 14a

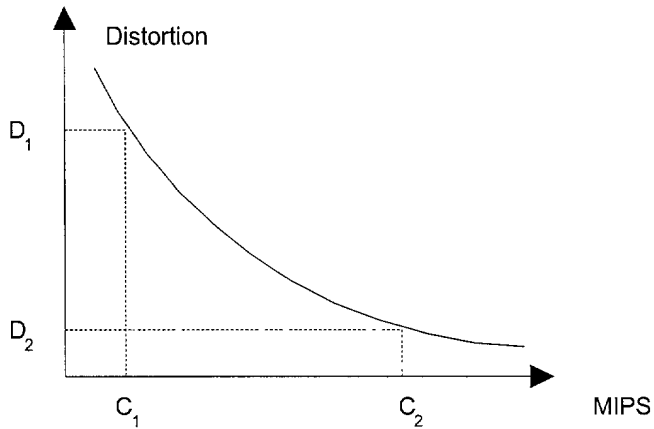


Fig. 14b

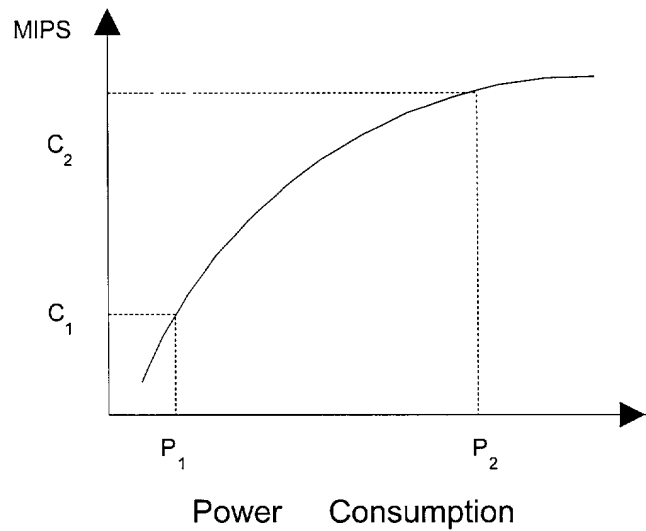


Fig. 15

Computation Time (ms)

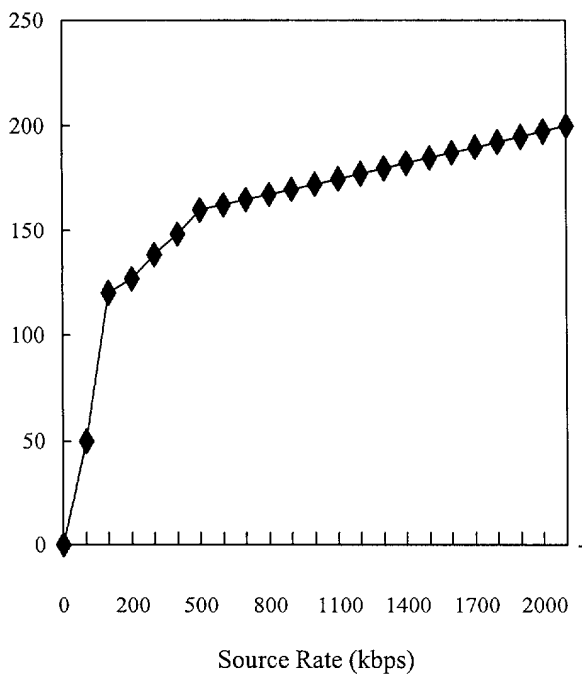


Fig. 16

Computational Time (ms)

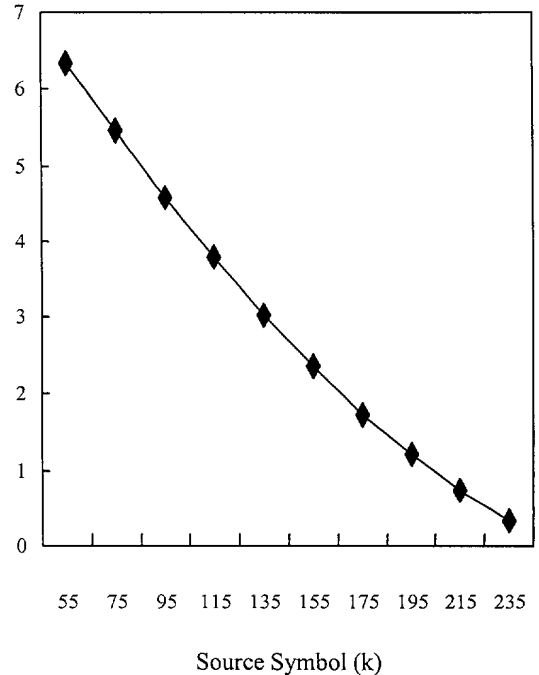


Fig. 17

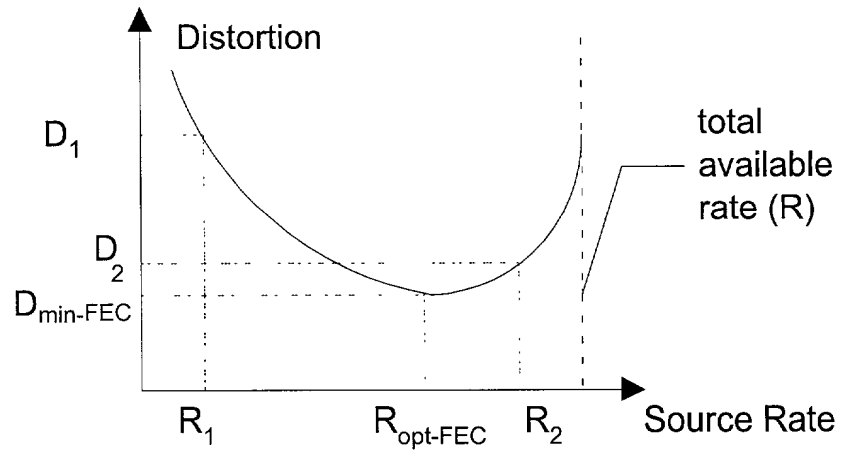


Fig. 18a

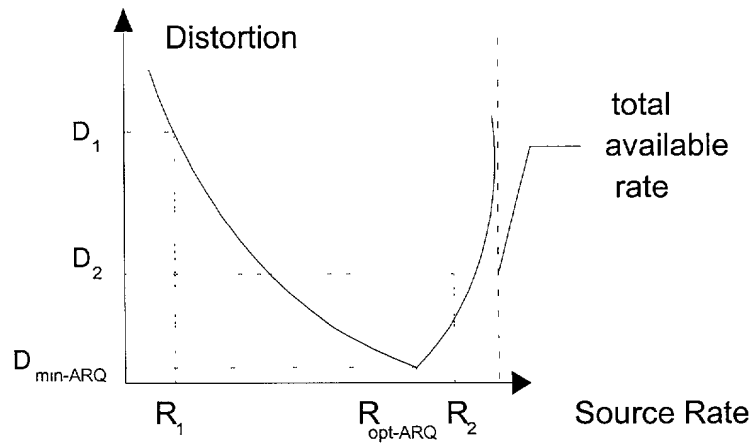
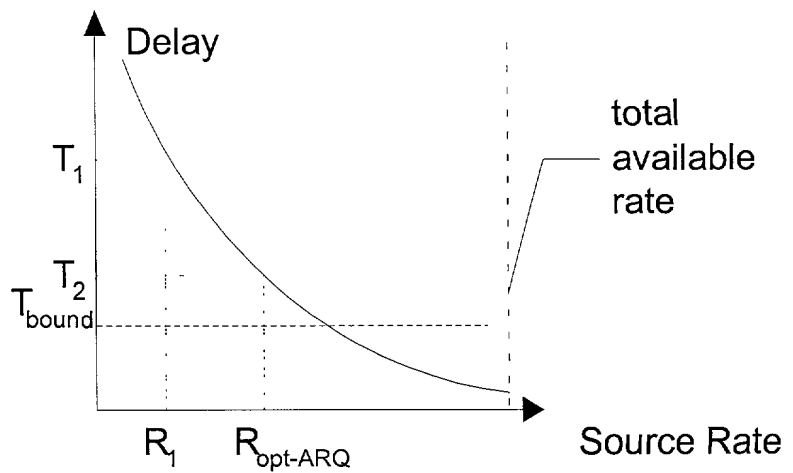
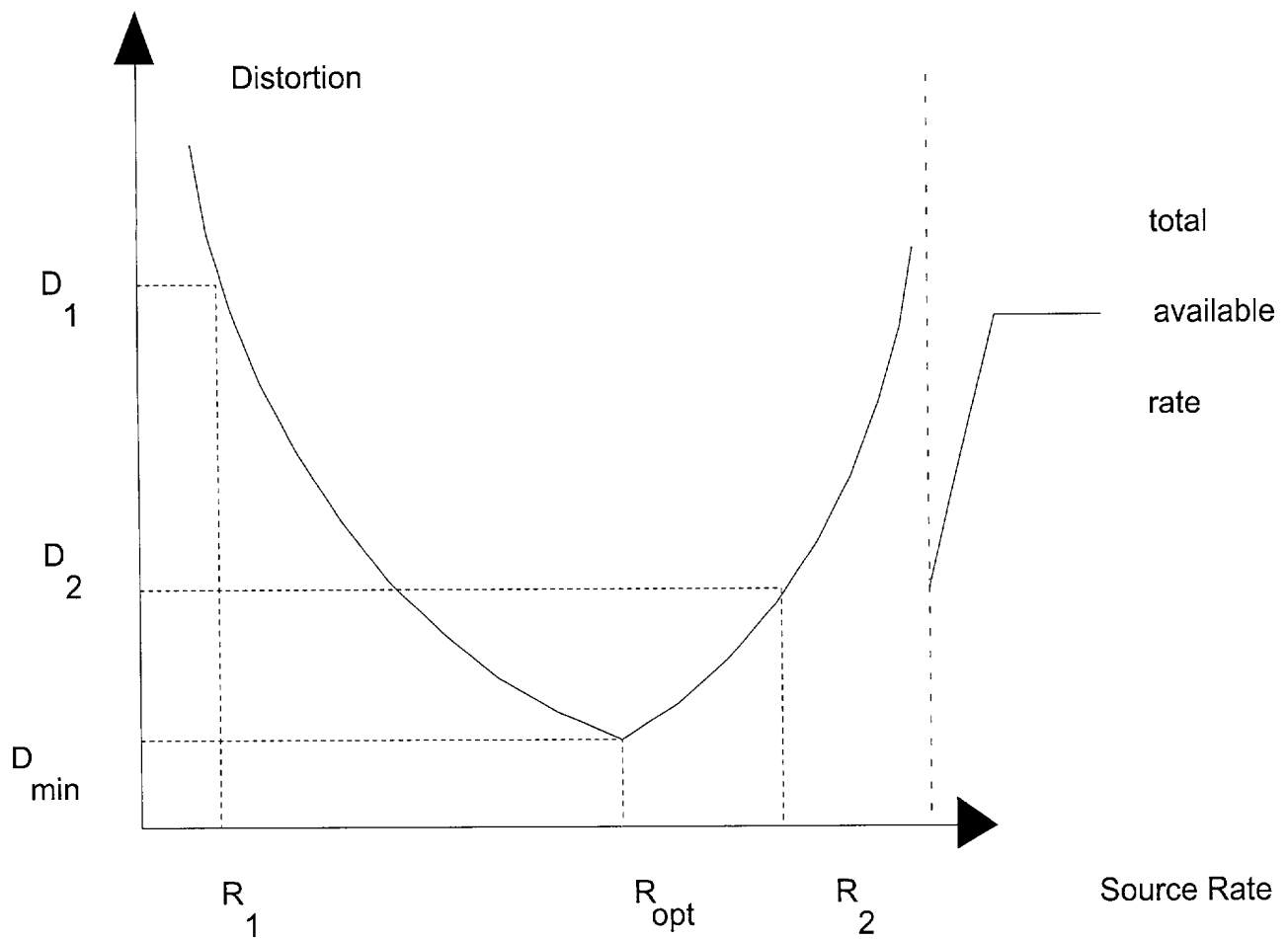


Fig. 18b

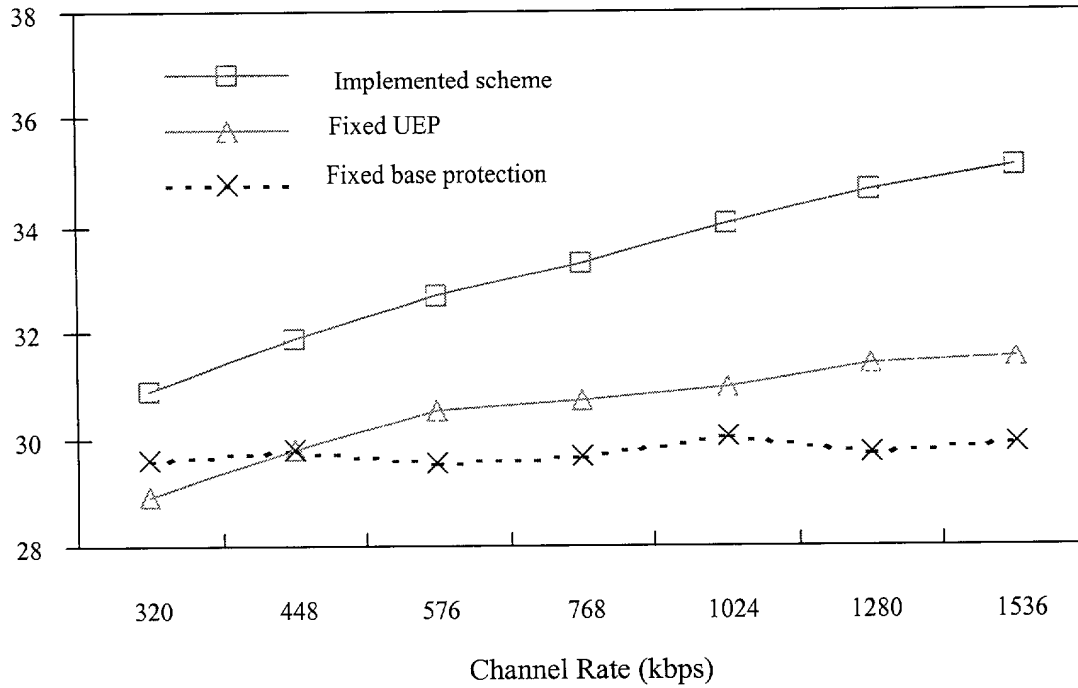


*Fig. 19*



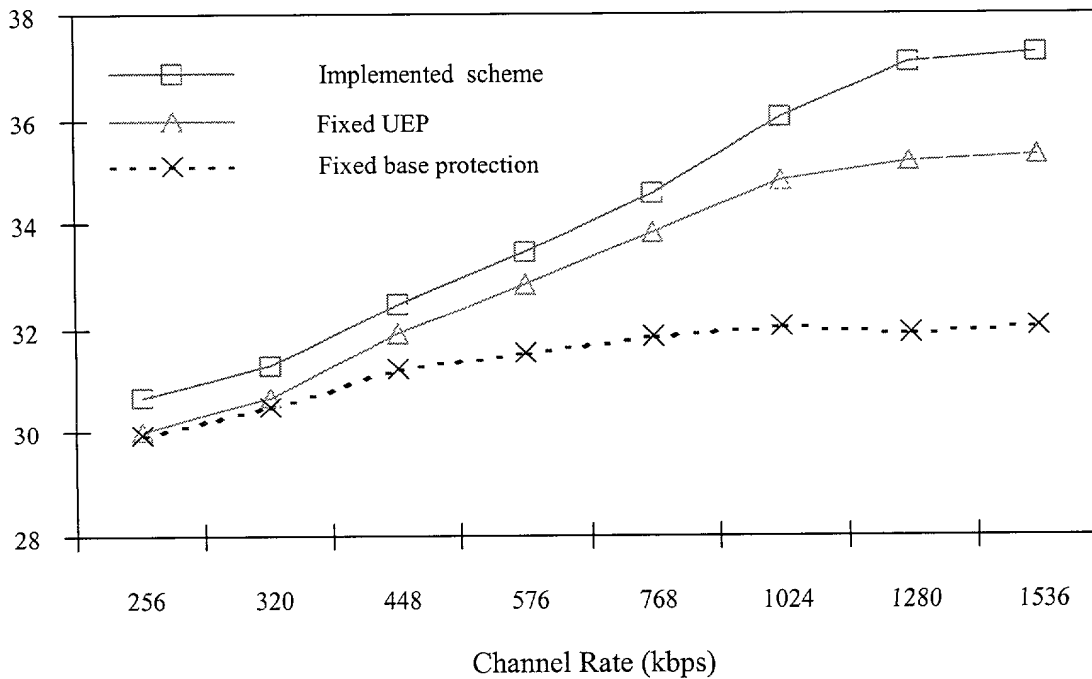
*Fig. 20a*

PSNR

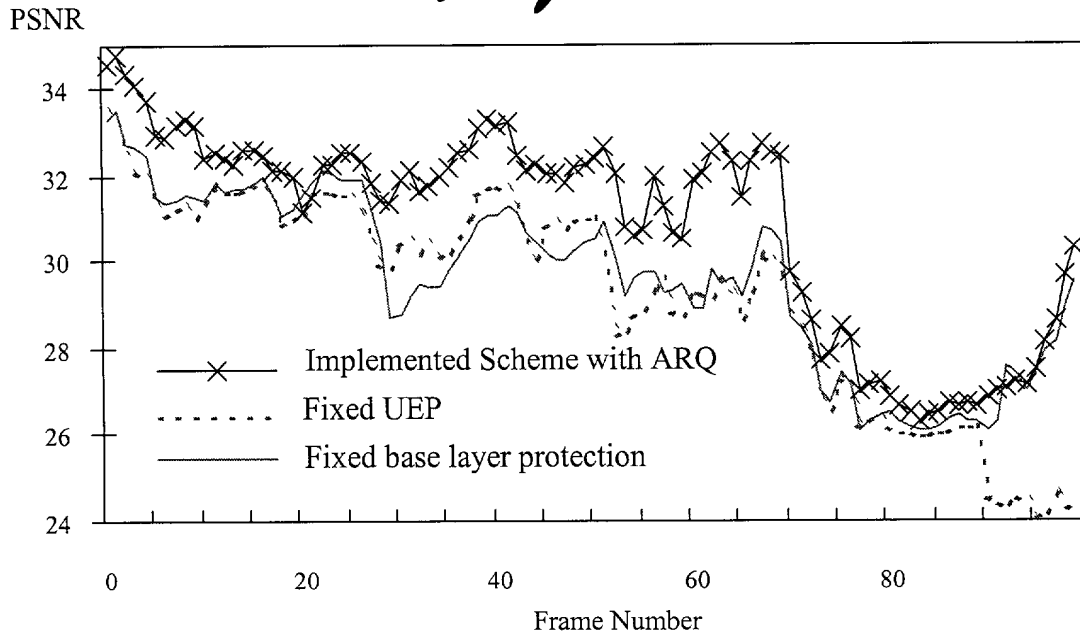


*Fig. 20b*

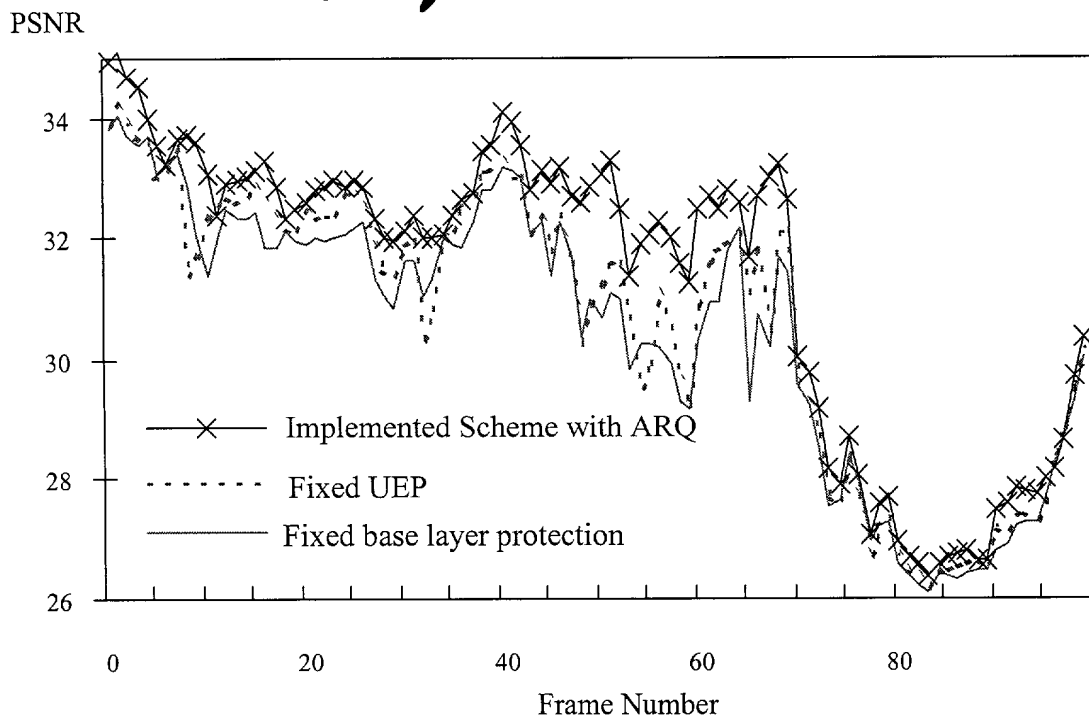
PSNR



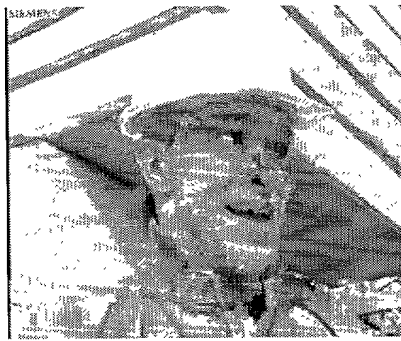
*Fig. 21a*



*Fig. 21b*



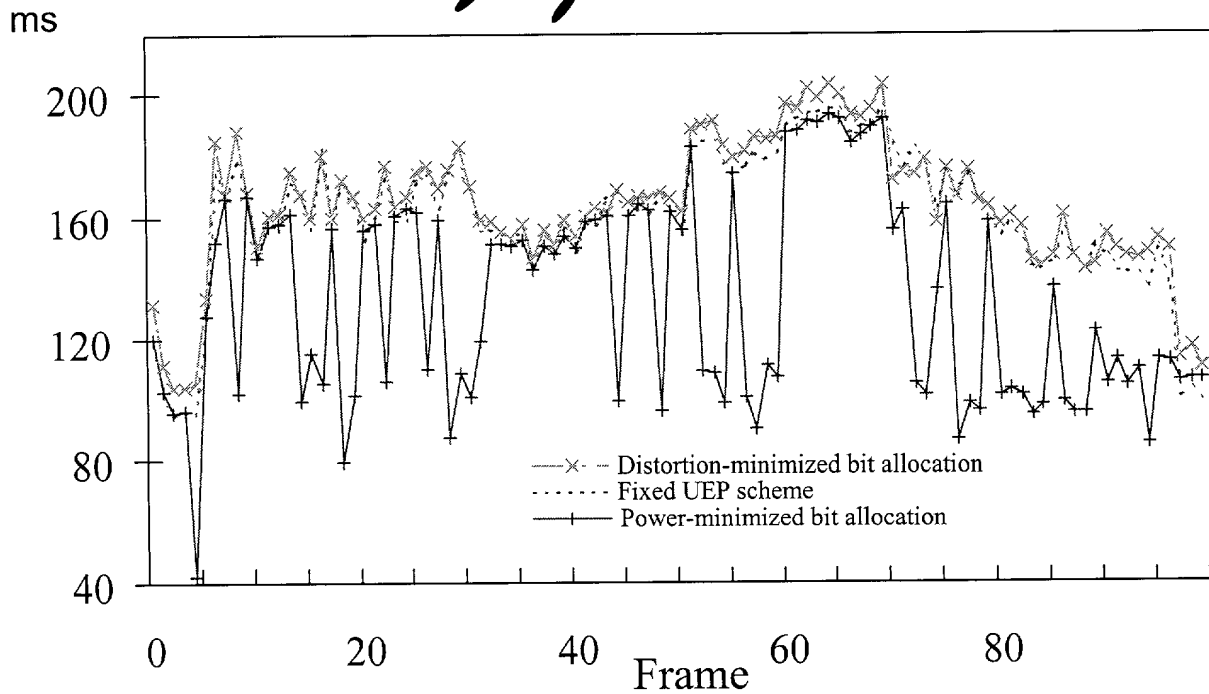
*Fig. 22a Fig. 22b Fig. 22c*



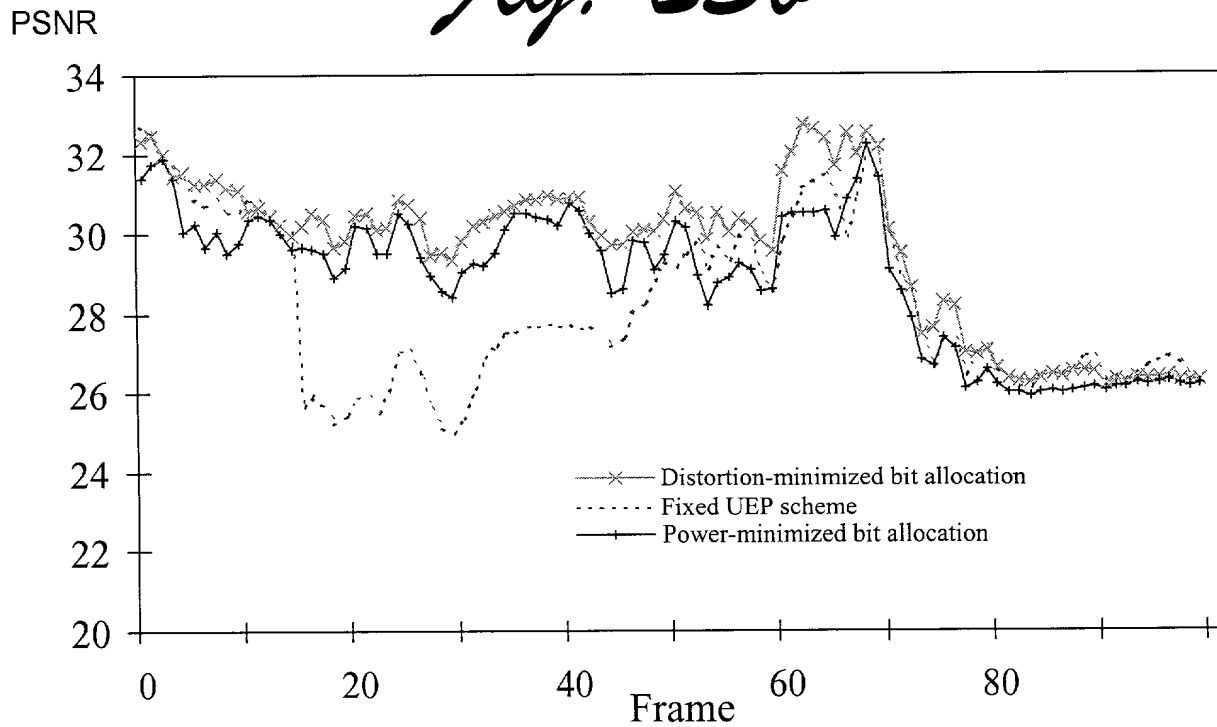
*Fig. 22d Fig. 22e Fig. 22f*



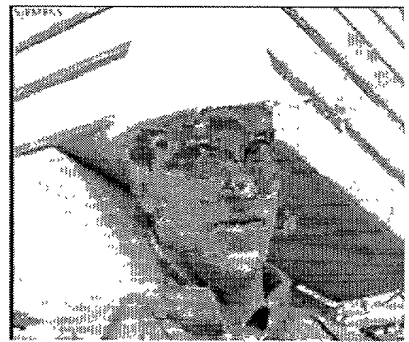
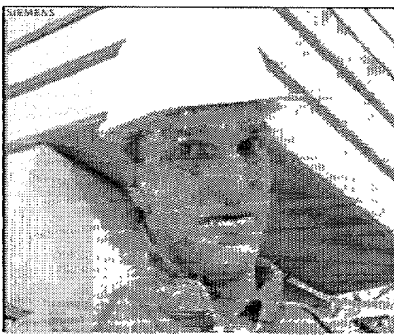
*Fig. 23a*



*Fig. 23b*



*Fig. 24a*      *Fig. 24b*      *Fig. 24c*



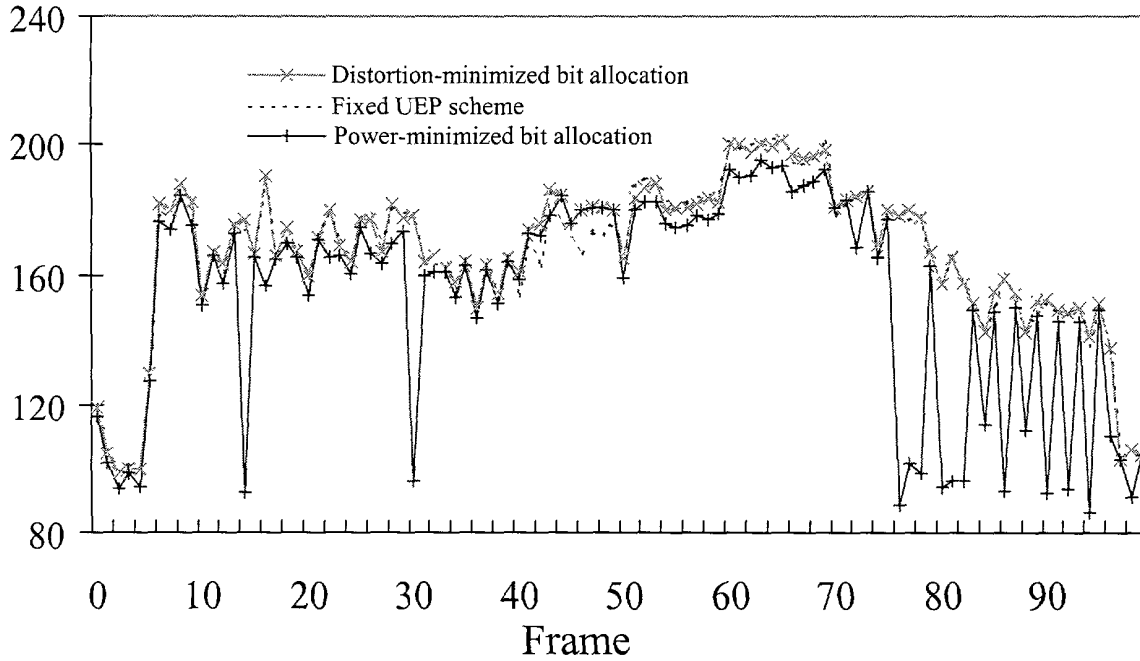
*Fig. 26a*      *Fig. 26b*      *Fig. 26c*





*Fig. 25a*

ms



*Fig. 25b*

PSNR

